

G. P. PATSIS has a degree in Physics (1995) from the Department of Physics, University of Athens, Master's degrees in Microelectronics (1997), Electronic Automation (1998) both from the Department of Informatics, University of Athens, and a PhD in Physics (1999) from the Department of Physical and Mathematical Sciences, National Technical University of Athens. He has authored/co-authored more than 70 articles in international refereed journals in the area of micro-fabrication, resist modelling-simulation, line-edge roughness metrology, and effects on devices. His work has been cited more than 1300 times by other researchers. Also, he has participated with more than 100 oral or poster presentations in various international refereed conferences on micro-electronics and nanotechnology (e.g. MNE, EIPBN, MN, MNC, SPIE). His research interests are in computational material science, microelectronic processes, and microelectronic circuits design/simulation in general.